



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

(Established by Govt. of Odisha in 1956 & Upgraded in 2009 to A State Govt. University Covered Under Section 2(f) & 12 (B) of UGC Act.)

P.O : Engineering College, Burla (Siddhi Vihar), Dist : Sambalpur Odisha -768018, INDIA

Ph : 0663-2430211, Fax : 0663-2430204 Website : www.vssut.ac.in

No. VSSUT/SRIC/2037/2026

Date: 14/10/2026

WALK-IN (OFFLINE) INTERVIEW PROJECT ASSOCIATE POSITIONS UNDER AICTE-RPS SCHEME

Title of Project: ASIC Solution for AI Based Precision Agriculture

The Veer Surendra Sai University of Technology, Burla invites candidates to attend a Walk-in Interview for the positions of **Project Associate (PA)** under the AICTE-RPS scheme. Interested candidates are advised to send a single PDF file containing the completed application form and supporting documents (Mark Sheets, Certificates, Research Publications, if any) via e-mail, mentioning the "Advertisement No." in the subject line, to the following email address: deansric@vssut.ac.in, akhota_etc@vssut.ac.in

Closing date of online application: **05.11.2026**

Date & Time of Interview: 11.11.2026, 10.00 AM

Venue: H.O.D. Office, Department of ETC Engg., VSSUT, Burla

Summary of Positions

Positions	Department- Vacancy	Duration	Monthly Fellowship/Stipend
PROJECT ASSOCIATE (PA)	Electronics & Telecommunication Engg - 01	Coterminous with the project	Rs. 30,000 pm for 03 Years(No HRA will be provided)

N.B. The PA who will be selected have to enroll into Ph. D Programme at VSSUT.

Candidates are required to bring the hard copy of duly filled and signed application form (available on the University website: www.vssut.ac.in) along with all supporting documents, including mark sheets, certificates, and research publications (if any) at the time of the Walk-in Interview. **No TA/DA will be paid for attending the interview.**

Memo No. VSSUT/Estt./SRIC/2038

Copy to:

1. The Director, I&PR Department, Government of Odisha, Bhubaneswar for information with a request to publish the above advertisement in all Odisha daily edition of the SAMAJ, the Dharitri and the SAMBAD on or before 20.10.2026.
2. University Notice Board.
3. The Dean, Faculty & Planning with a request to display the advertisement on the University website immediately.
4. The Dean, SRIC/COF for information & necessary action.
5. PA to V.C. for kind information of the Vice Chancellor.
6. PA to Registrar for kind information.

REGISTRAR

Dated: 14/10/2026

V.S.S. University of Technology
Burla, Sambalpur, Odisha

REGISTRAR

V.S.S. University of Technology
Burla, Sambalpur, Odisha-768018

ବୀର ସୁରେନ୍ଦ୍ର ସାଏ ଟେକ୍ନୋଲୋଜି ବିଶ୍ୱବିଦ୍ୟାଳୟ



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APPLICATION FORM FOR WALK-IN INTERVIEW JUNIOR RESEARCH FELLOW & PROJECT ASSOCIATE POSITIONS UNDER AICTE-RPS SCHEME

Paste here
your recent
passport size
photo

Fill all fields clearly in Block Letters.

1. Advertisement No. : _____

2. Project Code : _____

3. Post Applied For : _____

4. Department to which Applying for : _____

5. Name of the candidate in full (Block Capital letters) :

6. Father's Name : _____

7. Broad area only : _____

8. Name of Co-PI : _____

9. Address for Communication :

10. Telephone: _____ 11. Mob: _____ 12. Email: _____

13. Date of Birth: _____ 14. Sex: Male/Female 15. Marital status: Married /Single

16. Whether GEN/SC/ST: _____ 17. Nationality: _____

18. Mother Tongue: _____

Sp

. **Academic career:** (Enclose attested copies of the certificates and mark sheets/grade cards showing the percentage of marks (CGPA) from H.S.C Examinations or equivalent)

Name of Exams	Institute/University	Year of passing	Branch/Subject studied	Percentage of Marks/CGPA

Attach Supporting Documents

20. Whether Qualified GATE/NET (Yes/No): _____
If yes, mention GATE/NET Subject: _____ Qualifying Year: _____ Score: _____

21. Experience if any:

Organization	Position	Duration	Nature of job

22. Research Publications, if any: (Enclose copies of published or unpublished work. Enclose extra sheet if required)

I do hereby declare that the information furnished in this application is true to the best of my knowledge and belief. If selected, I shall abide by the rules and regulations of the University and the Hall allotted to me. If any information furnished in this application is found to be untrue, I am liable to forfeit the JRF/PA positions to me at any time in the future, and legal action may be taken against me.

Date: _____

Full signature of the Applicant

Enclosures:

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Detailed Project Matrix & Essential Qualifications

Broad Theme	Project Code	Position & Project Title	Essential & Desirable Qualification	Dept	PI Contact
Agriculture	211	[Project Associate (PA)] ASIC Solution for AI Based Precision Agriculture	Essential qualifications: B.Tech in Electronics / Electronics and Telecommunication / Electronics & Communication / Electronics & Instrumentation / Electrical & Electronics/ Computer Science Engineering AND M.Tech in Electronics / Communication Systems / Electronics & Communication / Electronics & Instrumentation / Electrical & Electronics/RF Engineering / RF and Microwave OR M.Sc in Electronics / Electronics and Telecommunication / Electronics & Communication/Computer Science Engineering Desirable qualification: B.Tech (GATE) / M.Sc (GATE / any national-level scholarship) with a CGPA of 7.0 or 65% marks. ME/M.Tech with at least 6.5 CGPA or 60% marks. Desirable soft skill: MATLAB, python, Cadence, HDL, FPGA Boards	ETC ENGG.	Dr. Aditya Kumar Hota akhota_etc@vssut.ac.in